ROHS AUML Varistor Series



Size Table

Metric	EIA
3216	1206
3225	1210
4532	1812
5650	2220

Applications

- Suppression of Inductive Switching or Other Transient Events Such as EFT and Surge Voltage at the Circuit Board Level
- ESD Protection for Components Sensitive to IEC 61000-4-2, MIL-STD-883C Method 3015.7, and Other Industry Specifications (See Also the MLE or MLN Series)
- Provides On-Board Transient Voltage Protection for ICs and Transistors
- Used to Help Achieve Electromagnetic Compliance of End Products
- Replace Larger Surface Mount TVS Zeners in Many Applications

Description

The AUML Series of Multilayer Transient Surge Suppressors was specifically designed to suppress the destructive transient voltages found in an automobile. The most common transient condition results from large inductive energy discharges. The electronic systems in the automobile, e.g. antilock brake systems, direct ignition systems, engine control, airbag control systems, wiper motor controls, etc., are susceptible to damage from these voltage transients and thus require protection. The AUML transient suppressors have temperature independent suppression characteristics affording protection from -55°C to 125°C.

The AUML suppressor is manufactured from semiconducting ceramics which offer rugged protection and excellent transient energy absorption in a small package. The devices are available in ceramic leadless chip form, eliminating lead inductance and assuring fast speed of response to transient surges. These Suppressors require significantly smaller space and land pads than silicon TVS diodes, offering greater circuit board layout flexibility for the designer.

Also see the Littelfuse ML, MLN and MLE Series of Multilayer Suppressors.

Features

- RoHS Compliant
- Load Dump Energy Rated per SAE Specification J1113
- Leadless, Surface Mount Chip Form
- "Zero" Lead Inductance
- Variety of Energy Ratings Available
- No Temperature Derating up to 125°C Ambient

- High Peak Surge Current Capability
- Low Profile, Compact Industry Standard Chip Size; (1206, 1210, 1812 and 2220 Sizes)
- Inherent Bidirectional Clamping
- No Plastic or Epoxy Packaging Assures Better than 94V-0 Flammability Rating

Absolute Maximum Ratings

For ratings of individual members of a series, see Device Ratings and Specifications chart.

Continuous	AUML Series	Units
Steady State Applied Voltage:		
DC Voltage Range (V _{M(DC)})	18	V
Transient:		
Load Dump Energy, (W _{LD})	1.5 to 25	J
Jump Start Capability (5 minutes), (V _{JUMP})	24.5	V
Operating Ambient Temperature Range (T _A)	-55 to + 125	°C
Storage Temperature Range (T _{STG})	-55 to + 150	°C
Temperature Coefficient (αV) of Clamping Voltage (V _C) at Specified Test Current	<0.01	%/°C

CAUTION: Stresses above those listed in "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress only rating and operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied.



Device Ratings and Specifications

	Maximum Ratings (125°C)			Specifications (25°C)				
Part Number	Maximum Continuous DC Voltage	Jump Start Voltage (5 Min)	Load Dump Energy (10 Pulses)	Nominal Varistor Voltage at 10mA DCTest Current		Maximum Standby Leakage (at 13V DC)	Maximum Clamping Voltage (V _C) at Test Current (8/20µs)	
	$V_{M(DC)}$	V_{JUMP}	$W_{\scriptscriptstyle{LD}}$	V _{N(DC)} Min V _{N(DC)} Max		I _L	V _C	l _P
	(V)	(V)	(J)	(V)	(V)	(μ A)	(V)	(A)
V18AUMLA1206	18	24.5	1.5	23	32	50	40	1.5
V18AUMLA1210	18	24.5	3.0	23	32	50	40	1.5
V18AUMLA1812	18	24.5	6.0	23	32	100	40	5.0
V18AUMLA2220	18	24.5	25	23	32	200	40	10.0

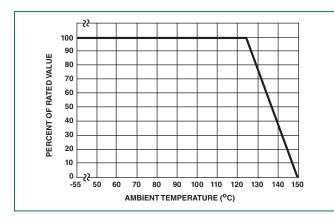
For automotive 24V and 42V applications please contact your Littelfuse representative or visit www.littelfuse.com for the latest product

NOTES:

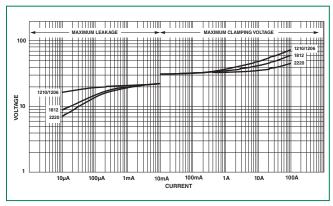
- 1. Average power dissipation of transients not to exceed 0.1W, 0.15W, 0.3W and 1W for model sizes 1206, 1210, 1812 and 2220 respectively.
- 2. Load dump energy rating (into the suppressor) of a voltage transient with a resultant time constant of 115ms to 230ms.
- 3. Thermal shock capability per Mil-Std-750, Method 1051: -55°C to 125°C, 5 minutes at 25°C, 25 Cycles: 15 minutes at each extreme
- 4. For application specific requirements, please contact Littelfuse.

Current, Energy and Power Derating Curve

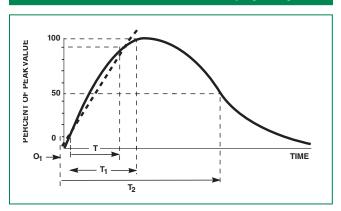
When transients occur in rapid succession, the average power dissipation is the energy (watt-seconds) per pulse times the number of pulses per second. The power so developed must be within the specifications shown on the Device Ratings and Characteristics table for the specific device. Certain parameter ratings must be derated at high temperatures as shown below.



Maximum Leakage Current/Clamping Voltage Curve for AUML Series at 25°C



Peak Pulse Current Test Waveform for Clamping Voltage



0, = Virtual Origin of Wave

T = Time from 10% to 90% of Peak

 $T_1 = Virtual Front Time = 1.25 x t$

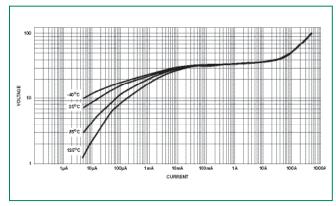
T₂ = Virtual Time to Half Value (Impulse Duration)

Example - For an 8/20 µs Current Waveform:

 $8 s = T_1 = Virtual Front Time$

 $20 \text{ s} = \text{T}_2 = \text{Virtual Time to Half Value}$

Typical V-I Characteristics of the V18AUMLA2220 at -40°C, 25°C, 85°C and 125°C



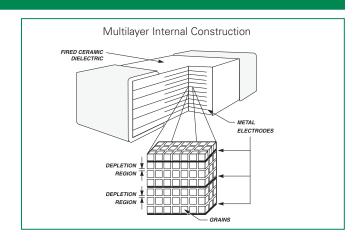
162 **AUML Varistor Series** Specifications are subject to change without notice. Please refer to www.littelfuse.com/series/AUML.html for current information

Varistor Products

Surface Mount Multilayer Varistors (MLVs) > AUML Series

Speed of Response

The clamping action of the AUML suppressor depends on a conduction mechanism similar to that of other semiconductor devices (i.e. P-N Junctions). The apparent slow response time often associated with transient voltage suppressors (Zeners, MOVs) is often due to parasitic inductance in the package and leads of the device and less dependent of the basic material (silicon, zinc oxide). Thus, the single most critical element affecting the response time of any suppressor is its lead induc-tance. The AUML suppressor is a surface mount device, with no leads or external packaging, and thus, it has virtually zero inductance. The actual response time of a AUML surge suppressor is in the 1 to 5 nanosecond range, more than sufficient for the transients which are likely to be encountered in an automotive environment.



Temperature Effects

In the leakage region of the AUML suppressor, the device characteristics approaches a linear (ohmic) relationship and shows a temperature dependent affect. In this region the suppressor is in a high resistance mode (approaching $10^6\Omega)$ and appears as a near open-circuit. Leakage currents at maximum rated voltage are in the microamp range. When clamping transients at higher currents (at and above the

ten milliamp range), the AUML suppressor approaches a 1-10 characteristic. In this region the characteristics of the AUML are virtually temperature independent. Figure 3 shows the typical effect of temperature on the V-I characteristics of the AUML suppressor.

Load Dump Energy Capability

A Load dump transient occurs when the alternator load in the automobile is abruptly reduced. The worst case scenario of this transient occurs when the battery is disconnected while operating at full rated load. There are a number of different load dump specifications in existence in the automotive industry, with the most common one being that recommend-ed by the Society of Automotive Engineers, specification #SAE J1113. Because of the diversity of these load dump specifications Littelfuse defines the load dump energy capability of the AUML suppressor range as that energy dissipated by the device itself, independent of the test circuit setup. The resultant load dump energy handling capability serves as an excellent figure of merit for the AUML suppressor. Standard load dump specifications require a device capability of 10 pulses at rated energy, across a temperature range of -40°C to 125°C. This capability requirement is well within the ratings of all of the AUML series (Figure 5).

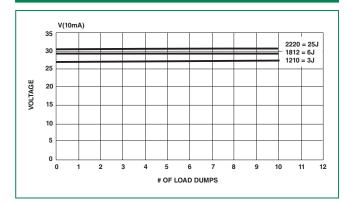
Further testing on the AUML series has concentrated on extending the number of load dump pulses, at rated energy, which are applied to the devices. The reliability information thus generated gives an indication of the inherent capability of these devices. As an example of device durabil-ity the 1210 size has been subjected to over 2000 pulses at its rated energy of 3 joules; the 1812 size has been pulsed over 1000 times at 6 joules and 2220 size has been pulsed at its rated energy of 25 joules over 300 times. In all cases there has been little or no change in the device characteristics (Figure 6).

The very high energy absorption capability of the AUML suppressor is achieved by means of a highly controlled manufacturing process. This technology ensures that a largevolume of suppressor material, with an interdigitated layer construction, is available for energy absorption in an extremely small package. Unlike equivalent rated silicon TVS diodes, the entire AUML device volume is available to dissipate the load dump energy.

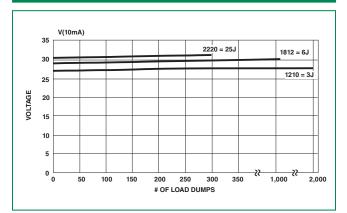
Hence, the peak temperatures generated by the load dump transient are significantly lower and evenly dissipated throughout the complete device (Figure 4). This even energy dissipation ensures that there are lower peak



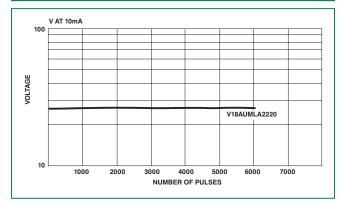
AUML Load Dump Pulsing over a Temperature Range of -55°C to 125°C



Repetitive Load Dump Pulsing at Rated Energy



Repetitive Energy Testing of the V18AUMLA2220 at an Energy Level of 2 Joules



Varistor Products

Lead (Pb) Soldering Recommendations

The principal techniques used for the soldering of components in surface mount technology are IR Re-flow & Wave soldering. Typical profiles are shown on the right.

The termination option available for each solder technique is:

Reflow Wave

1. Nickel Barrier (preferred) 1. Nickel Barr

2. Silver/Platinum

1. Nickel Barrier (preferred)

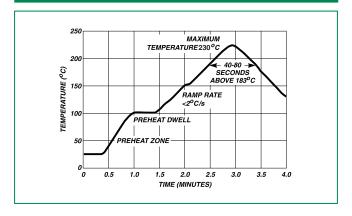
The recommended sol er for the AUML suppressor is a 62/36/2 (Sn/Pb/Ag), 60/40 (Sn/Pb) or 63/37 (Sn/Pb). Littelfuse also recommends an RMA solder flux.

Wave soldering is the most strenuous of the processes. To avoid the possibility of generating stresses due to thermal shock, a preheat stage in the soldering process is recommended, and the peak temperature of the solder process should be rigidly controlled.

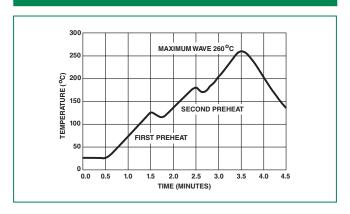
When using a reflow process, care should be taken to ensure that the AUML chip is not subjected to a thermal gradient steeper than 4 degrees per second; the ideal gradient being 2 degrees per second. During the soldering process, preheating to within 100 degrees of the solderis peak temperature is essential to minimize thermal shock.

Once the soldering process has been completed, it is still necessary to ensure that any further thermal shocks are avoided. One possible cause of thermal shock is hot printed circuit boards being removed from the solder process and subjected to cleaning solvents at room temperature. The boards must be allowed to cool gradually to less than 50°C before cleaning.

Reflow Solder Profile



Wave Solder Profile



Lead-Free (Pb-free) Soldering Recommendations

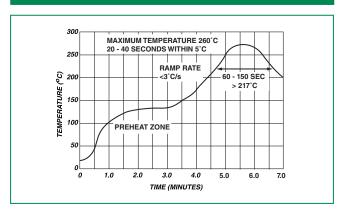
Littelfuse offers the Nickel-Barrier termination finish for the optimum Pb-free solder performance.

The preferred solder is 96.5/3.0/0.5 (SnAgCu) with an RMA flux, but there is a wide selection of pastes & fluxes available with which the nickel barrier parts should be compatible.

The reflow profile must be constrained by the maximums in the Lead-free Reflow Profile. For Pb-free Wave soldering, the Wave Solder Pfrofile still applies.

Note: the Pb-free paste, flux & profile were used for evaluation purposes by Littelfuse, based upon industry standards & practices. There are multiple choices of all three available, it is advised that the customer explores the optimum combination for their process as processes vary considerably from site to site.

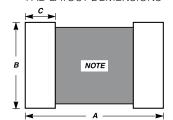
Lead-free Re-flow Solder Profile





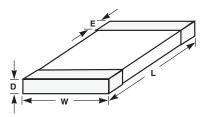
Dimensions

PAD LAYOUT DEMENSIONS



Note: Avoid metal runs in this area, parts are not recommended for use in applications using silver (Ag) expoxy paste.

CHIP LAYOUT DIMENSIONS



CVMDOL	1206		1210		1812		2220	
SYMBOL	IN	MM	IN	MM	IN	MM	IN	MM
А	0.203	5.150	0.219	5.510	0.272	6.910	0.315	8.000
В	0.103	2.620	0.147	3.730	0.172	4.360	0.240	6.190
С	0.065	1.650	0.073	1.850	0.073	1.850	0.073	1.850
D Max.	0.071	1.80	0.070	1.80	0.07	1.80	0.118	3.00
E	0.020 ± 0.010	0.50 ±0.25	0.020 ± 0.010	0.50 ± 0.25	0.020 ± 0.010	0.50 ± 0.25	0.030 ± 0.010	0.75 ± 0.25
L	0.125 ± 0.012	3.20 ± 0.03	0.125 ± 0.012	3.20 ± 0.30	0.180 ± 0.014	4.50 ± 0.35	0.225 ± 0.016	5.70 ± 0.40
W	0.060 ± 0.011	1.60 ± 0.28	0.100 ± 0.012	2.54 ± 0.30	0.125 ± 0.012	3.20 ± 0.30	0.197 ± 0.016	5.00 ± 0.40

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Explanation of Terms

Maximum Continuous DC Working Voltage (*V_{M*/DCU.+})

This is the maximum continuous DC voltage which may be applied, up to the maximum operating temperature (125°C), to the ML suppressor. This voltage is used as the reference test point for leakage current and is always less than the breakdown voltage of the device.

Load Dump Energy Rating *W, D+

This is the actual energy the part is rated to dissipate under load dump conditions (not to be confused with the "source energy" of a load dump test specification).

Maximum Clamping Voltage *V_{C+}

This is the peak voltage appearing across the suppressor when measured at conditions of specified pulse current and specified waveform (8/20µs). It is important to note that the peak current and peak voltage may not necessarily be coincidental in time.

Leakage Current *I.

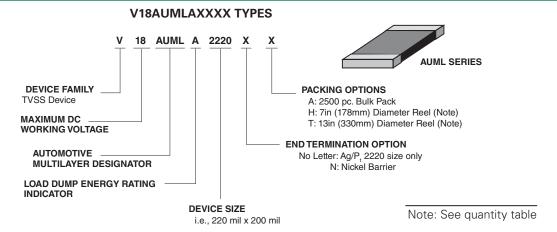
In the nonconducting mode, the device is at a very high impedance (approaching $10^6\Omega$ at its rated working voltage) and appears as an almost open circuit in the system. The leakage current drawn at this level is very low (<25µA at ambient temperature) and, unlike the zener diode, the multilayer TVS has the added advantage that, when operated up to its maximum temperature, its leakage current will not increase above 500μ A.

Nominal Voltage *V_{N*DC+}+

This is the voltage at which the AUML enters its conduction state and begins to suppress transients. In the automotive environment this voltage is defined at the 10mA point and has a minimum ($V_{N(DC) \text{ MAX}}$) voltage specified.

Varistor Products

Part Numbering System

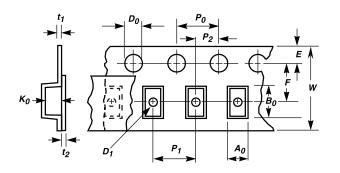


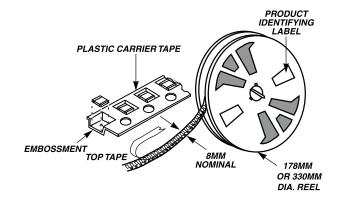
Packaging

Device Size	13" Inch Reel ("T" Option)	7" Inch Reel ("H" Option)	Bulk Pack ("A" Option)
1206	10,000	2,500	2,500
1210	8,000	2,000	2,000
1812	4,000	1,000	1,000
2220	4,000	1,000	1,000



Tape and Reel Specifications





Symbol	Description	Dimensions in Millimeters		
A ₀	Width of Cavity	Dependent on Chip Size to Minimize Rotation.		
B ₀	Length of Cavity	Dependent on Chip Size to Minimize Rotation.		
K ₀	Depth of Cavity	Dependent on Chip Siz	e to Minimize Rotation.	
W	Width of Tape	8 ±0.2 12±0.2		
F	Distance Between Drive Hole Centers and Cavity Centers	3.5 ±0.5	5.4±0.5	
Е	Distance Between Drive Hole Centers and Tape Edge	1.75 ±0.1	1.75 ±0.1	
P ₁	Distance Between Cavity Center	4 ±0.1	8±0.1	
P ₂	Axial Distance Between Drive Hole Centers and Cavity Centers	2 ±0.1	2 ±0.1	
P_0	Axial Distance Between Drive Hole Centers	8±0.1	8±0.1	
D_{0}	Drive Hole Diameter	1.55 ±0.05	1.55 ±0.05	
D ₁	Diameter of Cavity Piercing	1.05 ±0.05	1.55±0.05	
T ₁	Embossed Tape Thickness	0.3 Max	0.4 Max	
T ₂	Top Tape Thickness	0.1 Max	0.1 Max	

NOTE: Dimensions in millimeters.

- Conforms to EIA-481-1 Revision A
- Can be supplied to IEC publication 286-3

Tape	8mm W	ide Tape	12mm Wide Tape		
Chip Size	1206	1210	1812	2220	

Standard Packaging

Tape and Reel is the standard packaging method of the AUML series. The standard 300 millimeter (13 inch) reel utilized contains 4000 pieces for the 2200 and 1812 chips, 8000 pieces for the 1210 chip and 10,000 pieces for the 1206 size. To order add "T" to the standard part number, e.g.V18AUMLA222OT.

Special Packaging

Option1: 178 millimeter (7 inch) reels containing 1000 (2220, 1812), 2000 (1210), 2500 (1206), pieces are available. To order add "H" to the standard part number, e.g. V18AUMLA2220H.

Option 2:For small sample quantities (less than 100 pieces) the units are shipped bulk pack. To order add "A" to the standard part number, e.g. V18AUMLA2220A.